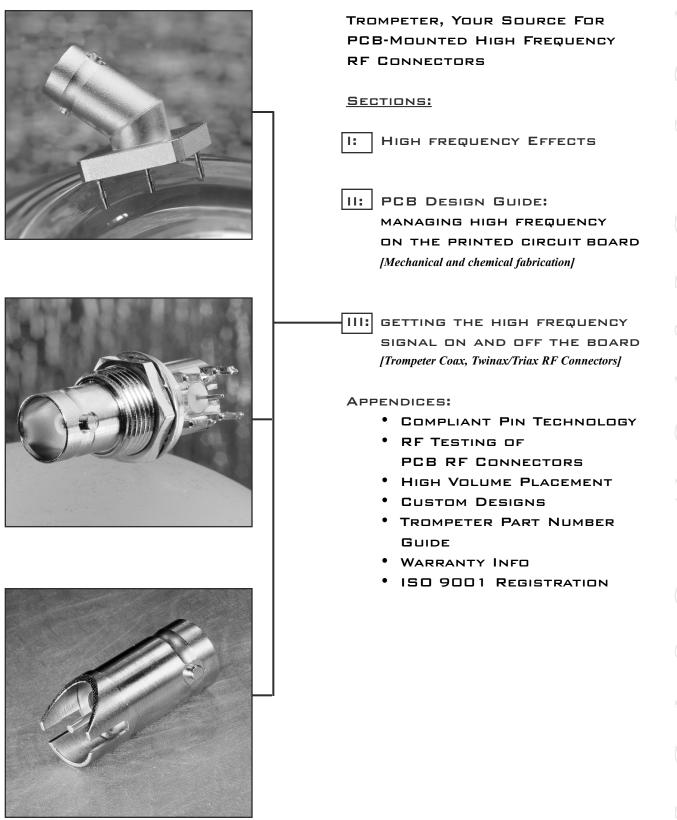
# PCB DESIGN GUIDE

# ENGINEERING HIGH FREQUENCY APPLICATIONS AT THE BOARD LEVEL USING RF CONNECTORS



## PCB-MOUNTED HIGH FREQUENCY RF CONNECTORS

Photo: Multiplex 1553 Bus Switch which connects multiple MIL-STD-1553 Resources Across ocal and Extended Buses loaded with Trompeter printed circuit board connector CBBJR79.

Photo courtesy of: Systran Corp

Founded in 1960, Trompeter originally focused on addressing RF patching needs for real-time data instrumentation for the US Navy. Since then, Trompeter has evolved into a full service supplier of interconnect products offering over 5,000 unique products.

As frequency and density of signal-carrying electronic transmission lines have risen, so has the need for separable interconnects to meet these performance characteristics. Trompeter's contribution to the industry has been delivering products for special markets with tough demands for signal integrity and durability. Product offerings include coax, twinax and triax connectors, cables, cable assemblies and tools. Trompeter's mil/aero products meet MIL-C-49142, MIL-STD-1553B data bus and MIL-C-39029 standards.

This design guide features our newest line of printed circuit board jacks for RF shielded interconnection requirements. It includes a line of standard products as well as custom connector design services for special requirements. Trompeter's engineering staff is equipped with the latest in computer RF modeling and drafting tools to provide custom solutions quickly and efficiently.

All of Trompeter's products are manufactured in our modern 60,000 sq. ft. factory located in Westlake Village, CA. The company is registered for ISO 9001 in the US and Europe through the DNV.

Contact us directly at 800-982-2629 or visit us at www.trompeter.com for more information, quotes or assistance with your design challenges.

# SECTION I - HIGH FREQUENCY EFFECTS

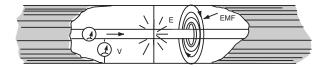
#### WHAT DOES RF MEAN?

RF stands for radio frequency (or radar frequency, depending on your background), and typically refers to bands between 500 MHz and 2 GHz, where traditional microwave takes over.

#### Figure 1.

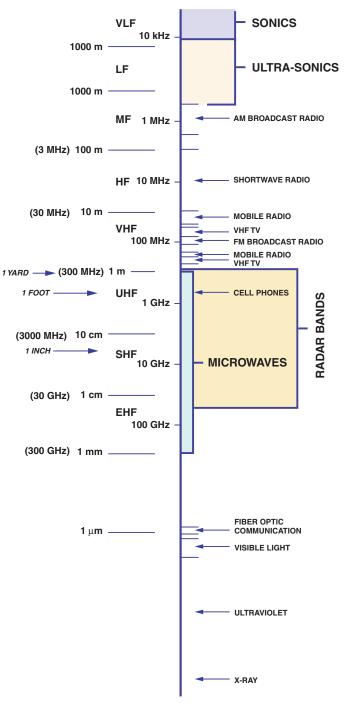
| RF is <u>R</u> adio or <u>R</u> adar <u>F</u> requency          |   |  |  |
|---|---|--|--|
| Radio band  | Radar band  |  |  |
| 88 - 108 MHz = FM<br>30 - 300 MHz = VHF<br>300 - 3000 MHz = UHF | 300 - 1000 MHz = UHF<br>8 - 10 GHz = X band<br>12.5 - 18 GHz = Ku |  |  |
| IICIT Tutorial - High Frequency and RF Connectors               |   |  |  |

More generally, it has come to stand for electrical signals sent at high frequency over a controlled impedance line, using ground or shielding to prevents signal degradation. Coax cable is an example of the more general definition.



RF effects occur when current carrying wires become transmission lines with electromagnetic fields. The resulting field is of minor importance at lower frequencies. At higher frequencies these fields and related "effects" such as return loss, VSWR (Voltage Standing Wave Ratio), skin effect, and insertion loss and ... they matter a great deal.

Connectors are critical to a successful transmission line in that they must perform their mating assignment without degrading performance. For separable RF connectors, this is even more critical.



*Figure 2.* The Wave Spectrum: wave length and frequency

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#### WHAT IS RETURN LOSS?

When a signal travels down a wire (also called a "trace" in PCB terminology or a "transmission line" in microwave terminology), at every place that a discontinuity or mismatch load appears, some portion of that signal is reflected back toward the source.

Discontinuities are things like abrupt changes in direction (a 90° turn in signal trace), in geometry (small wire to a large connector), or in impedance.

The reflected, "returning" signal bounces back down the line it came in on and sums to the existing inbound or incident wave, and may create distortions in wave form and intensity. As a result, the signal can be degraded or even totally lost.

Return loss is measured in decibel, a logarithmic ratio, and is a negative number (-30dB is better than -15 dB in return loss).

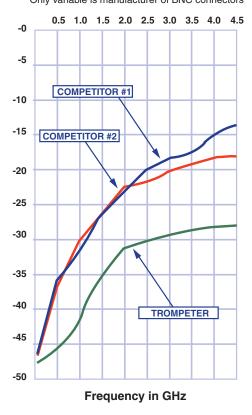
At sub-RF frequencies, these reflections are minor. At high frequency they can seriously alter the "look" of the inbound signal by being out of phase and, worse case, can "cancel" the inbound signal so significantly that it looks like it isn't even present (a standing wave).

**Return Loss and VSWR** - Return Loss has two detrimental components: (1) signal (energy) which never gets to its intended destination, and (2) energy which can cancel out or otherwise distort the incoming signal. VSWR (pronounced vis'-wahr) is a unit-less ratio relating reflected voltage to incident voltage, most notable at impedance mismatches. In a worse case scenario, the reflected wave is equal to the incident wave and 180 degrees out of phase, resulting in a perfect standing wave where no action takes place. VSWR can be related directly to return loss for a given frequency, see Figure 3. For transmission lines, a return loss of 20 dB is exceptionally good. Although return loss is mathematically a negative number, it is often abbreviated as an absolute number.

| Figure 3. | Conversion | of Return | Loss to | VSWR |
|-----------|------------|-----------|---------|------|
|           |            |           |         |      |

| <u>Return Loss</u> | <u>VSWR</u> |  |
|--------------------|-------------|--|
| -40 dB             | 1.020       |  |
| -30 dB             | 1.065       |  |
| -25 dB             | 1.119       |  |
| -20 dB             | 1.222       |  |
| -17 dB             | 1.329       |  |
| -15 dB             | 1.433       |  |
| -13 dB             | 1.577       |  |
| -11 dB             | 1.785       |  |

**BNC CONNECTOR RETURN LOSS DATA** Only variable is manufacturer of BNC connectors



*Figure 4. RF Connector Return Loss Comparisons.* 

#### SKIN EFFECT

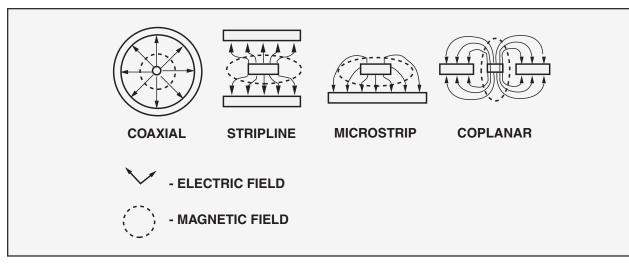
With low frequency designs, the sizing of the conductor itself has to do with the amount of tolerable temperature rise allowed in the system, as conductors carry energy without perfect conductivity. At some power level, all conductors become resistors and create heat. For low frequency designs, this is a known and attributable loss value for energy. Not so with high frequency signal management.

## Figure 5. Skin Depth

| <u>One Skin Depth</u> | <u>Frequency</u> |
|-----------------------|------------------|
| 0.315000 inches       | 60 hertz         |
| 0.027000 inches       | 10 kilohertz     |
| 0.000790 inches       | 10 megahertz     |
| 0.000028 inches       | 10 gigahertz     |

As frequency rises, energy moves to the outside surface or "skin" of the conductor as the transport media, see Figure 4. Three skin depths handles about 98% of the total energy in the signal. At some point, the energy is largely in the device plating rather than the base metal itself. The higher the frequency, the more pronounced the "skin effect".

Figure 6. Transmission Lines

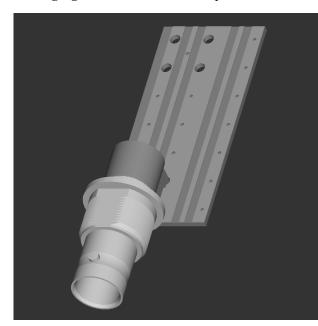


#### IMPEDANCE

Impedance is the ratio of voltage to current in a traveling wave. In a coaxial wireline this is related to the dielectric properties of the insulating material, the diameter of the center conductor and the spacing of the shield.

As frequency goes up, the need to control the electromagnetic fields goes up.

*Figure 7. Trompeter UCBBJR229 -Managing the Coax to Microstrip Transition* 



#### PROPAGATION VELOCITY

The fastest possible velocity of the speed of light is achieved in a vacuum. Signals slow, or experience loss, in every other environment. Materials have a property called dielectric constant, also known as permitivity, epsilon or Dk. Some of these values and the corresponding speeds are shown in Figure 8 below.

# *Figure 8.* Selected Dielectric Materials Velocity of Propagation

| <u>Material</u>                                   | <u>Dk value</u> | <u>Speed (in/ns)</u> |  |
|---|-----------------|----------------------|--|
| Air (~vacuum)                                     | 1.0             | 12.0                 |  |
| PTFE (Teflon <sup>TM</sup> )                      | 2.1             | 8.1                  |  |
| Polymide (Kapton)                                 | 3.5             | 6.4                  |  |
| Epoxy   | 4.7             | 5.5                  |  |
| Water   | 70.0            | 1.4                  |  |
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Electrical signals are similar to light in that their transmission speed in a perfect dielectric (deep space) is ~186,000 miles/sec. The lower the Dk, the faster the signal propagation. PTFE is one of the best materials that can structurally support the critical dielectric spacing necessary for RF designs.



#### INSERTION LOSS (ATTENUATION)

Attenuation is an electronics name for "loss", the total amount by which power received is less than power transmitted, after a device has been inserted. This applies to the entire connector / cable system. In any signal path, energy is used up in conductor losses (transformed to heat), dielectric losses, reflection, and radiation. The signal is thus "attenuated" in energy level. Loss is measured in dB, usually expressed in dB per unit of length, as distance plays a major role in loss values.

For longer distance transmission lines, properties of attenuation are crucial. Most coax cable, for example, is rated based on attenuation per 100 meters. For connectors, attenuation is of less importance as the signal is not "in" the connector for long, relative to the time lapse in the cable.

In some cases, it is desirable to produce a full attenuation of an incoming signal. This is called a "load" and is typically a precision termination resistor that is matched to and absorbs the incoming signal, eliminating reflection back to the source.



Figure 9. Example of low frequency coax connector. Note parallel leads which cause a serious "cross talk" problem at higher frequency. Trompeter does not make any PCB-mounted connectors like this.

#### BANDWIDTH

Bandwidth is a measure of capacity that describes low-to-high frequencies used to transport signals. According to individual applications, bandwidth requirements vary widely. For example, the audio bandwidth of a telephone analog signal is 3kHz and the bandwidth of an audio CD is 20kHz (which explains why CD audio sounds so much better than a voice over the telephone)!

More bandwidth requires more frequencies to accommodate the signal. Since the usable low end of the bandwidth is relatively fixed, as a practical matter higher bandwidth means higher frequency.

The science of electronics changes when signal speeds (also called propagation velocities) get into the RF zone and above. Laws of physics dictate that new issues be dealt with to enable successful transmission of a signal down a wire. As frequency gets higher, wave size diminishes and electromagnetic effects are more troublesome. Ohms law.

#### INDUCED SIGNALS

Energy moving down a conductor (transmission line) induces an electromagnetic field,

increasingly so as frequency rises. The reverse is also true, that unwanted electromagnetic radiation may cause an unwanted signal that can interfere with the incident signal. The EMI or RFI (electromagnetic interference or radio frequency interference) is part of what RF design engineers must solve in high frequency designs.

#### RISE TIMES

In digital technology, a primary driver for the use of higher frequency is to achieve higher performance via shorter rise times, see Figures 13-17.

#### Figure 10. How Much Bandwidth?

- Computer LAN (local area network) the typical Ethernet data rate is 100 Mbps
- Gigabit Ethernet is 1000 Mbps
- Comparative data rates using 25MB movie "trailer" as the test case
  - At 100 Mbps, 10 seconds to download
  - On a T1 line, this would take 8 minutes.

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Figure 11. EMI and RFI

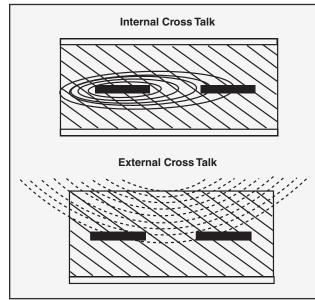


Figure 12. Typical Device Rise Times

|                      | Output Pulse            |
|----------------------|-------------------------|
| <u>Device Family</u> | <u>Rise Times, nsec</u> |
| TTL                  | 6 to 9                  |
| Schottky TTL         | 2 to 3                  |
| ECL                  | 0.45 to 0.75            |
| GAAS                 | 0.05 to 0.20            |
|                      |                         |

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# SECTION I - HIGH FREQUENCY EFFECTS

FOURIER (4-E-A) APPROXIMATION

- Waves have harmonics.
- Always use odd number values such as the 7th harmonic. The "clock" rate is the pulse generator of a wave. In order that the wave be "square", higher harmonic wave forms of that same pulse are used. Note that this is no longer time domain (clock speed).
- To convert rise times to frequency, use the relationship "Frequency = 0.35/rise time". If rise time is in nanoseconds, frequency will be in gigahertz (GHz).

Figure 13. Fundamental Clock Rate Wave Form

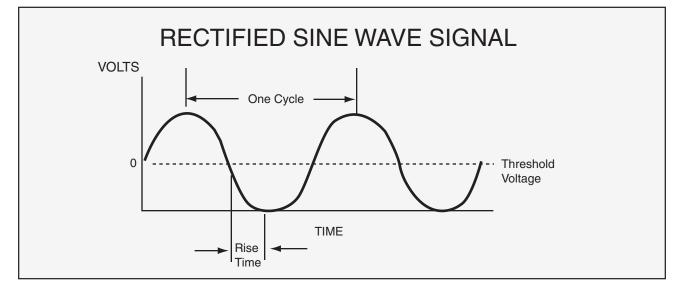
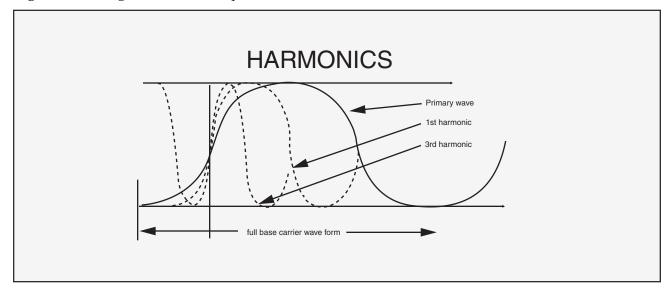
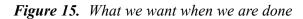


Figure 14. Using Harmonics To Square A Sine Wave



# SECTION I - HIGH FREQUENCY EFFECTS



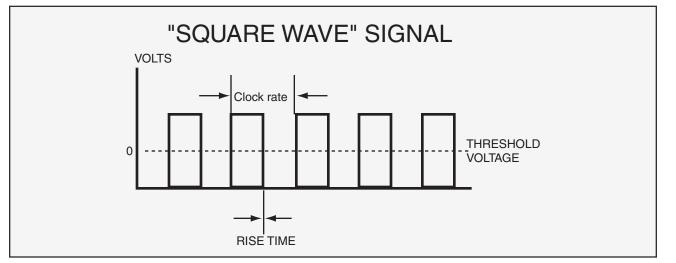


Figure 16. How We Get There: Sum (stacking up) the Harmonics

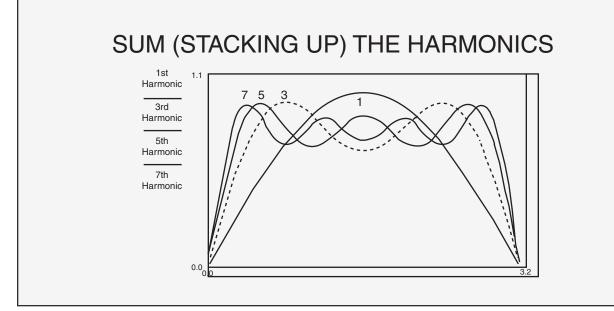
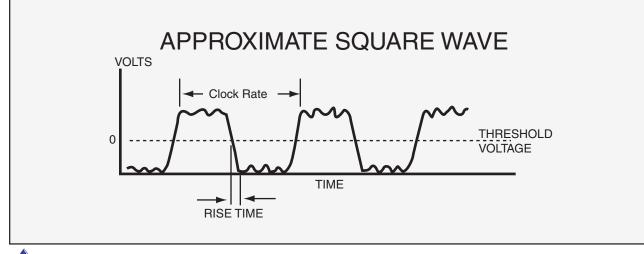


Figure 17. Results of using harmonics



### MECHANICAL FABRICATION

#### LAMINATE SELECTION

New circuit constructions incorporate traditional low frequency materials (FR-4, G-10, Polyimide, Cyanate Ester) as well as high performance materials (woven-glass Teflon<sup>TM</sup>, R04003<sup>TM</sup>, Duriods<sup>TM</sup>). This technique is becoming more common as a method of achieving board performance objectives related to propagation velocity and dielectric loss.

## COPPER FOIL SELECTION AND IMPACT ON MANUFACTURABILITY/ PERFORMANCE.

The choice of as-laminated copper foil cladding thickness and type should be governed by the end use. From a high frequency performance standpoint, thinner copper foil can be specified because most of these signals do not require the complete foil thickness to travel efficiently. At high frequencies, most of the current is carried in the thin outermost "skin" of the conductor. Generally speaking, two-thirds of the available signal travels in the outermost skin depth.

From a manufacturability standpoint, thinner copper is preferred because it requires less etching and thus improves attainable tolerances on etched line widths (see Figure 18). Note that when parts are copper plated during fabrication, the additional Z direction plated thickness may have to be considered in defining the line/gap etching tolerances (depending upon whether pattern plating on panel plating processing is used).

As a general rule, the total etched feature tolerance band is 100% to 200% of the total thickness of copper that must be etched to define the circuitry, depending on etch process capability.

| ETCHED LINE TOLERANCES               |                               |  |  |  |
|--------------------------------------|-------------------------------|--|--|--|
| Foil<br>Copper<br>Weight             | Plated<br>Copper              | Surface<br>Copper<br>Thickness               | Surface Etch<br>Bilateral<br>Tolerance           |  |
| 0.5 oz.<br>1 oz.<br>0.5 oz.<br>1 oz. | None<br>None<br>Yes*<br>Yes** | 0.7 mils<br>1.4 mils<br>2.0 mils<br>2.7 mils | ±0.5 mils<br>±1.0 mils<br>±1.5 mils<br>±2.0 mils |  |

- \* 0.5 oz. foil with 1 mil of plated copper minimum in the hole.
- \*\* 1 oz. foil with 1 mil of plated copper minimum in the hole.

Note that variations in foil copper thickness can be as much as  $\pm 10\%$  on the laminate material as supplied to some printed circuit board companies.

#### MECHANICAL FABRICATION

#### MECHANICAL FABRICATION

The cost impact of providing vias, access cavities and peripheries in printed circuits cannot be overstated. While most machining methods for printed circuit boards are quite mature, recent advances in laser machining have provided new options that should be considered. Care should be taken to specify reasonably achievable feature dimensions and tolerances. The printed circuit board industry prefers to use geometric tolerancing as specified by ANSI-Y14.4M, since it allows a tolerance "budget" that can be distributed among many factors. Although bilateral tolerancing is still used by some, most newer designs incorporate geometric tolerancing guidelines. For a more thorough discussion of the merits of each tolerancing method, refer to the section on true dimension tolerancing (see Appendix).

#### DRILLED HOLES

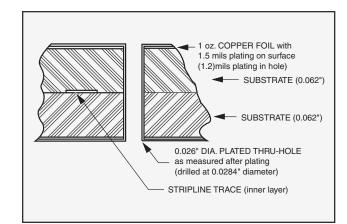
Minimum hole diameters are generally dictated by the overall material thickness. A difficulty factor often cited is the aspect ratio (material thickness to hole diameter) see Figure 19. As a general rule, aspect ratios of  $\sim$ 3 are easy, of  $\sim$ 5 are difficult, and of  $\sim$ 10 are extremely difficult to fabricate, depending on the material thickness; i.e. higher aspect ratios provide less difficulty on thinner materials.

Higher aspect ratio holes are more difficult to drill. For smaller drill sizes (<13 mils), drill breakage and hole roughness can be a substantial problem. In addition, higher aspect ratio holes are difficult to clean, activate, and plate.

Where high aspect ratios are a necessity, manufacturability can be improved by countersinking the hole to lower the effective minimum aspect ratio. Ability to hold positional tolerances varies significantly according to material type (inherent dimensional stability), thickness, and overall part dimensions. A true position diameter of 10 mils is most common and readily attained.

Whenever appropriate, the maximum material condition (MMC) should be specified to permit balancing hole diameter tolerances and positional tolerances to increase manufacturability.

*Figure 19.* Cross-section side view of a 5:1 aspect ratio plated hole in a stripline design. The formula for aspect ratio is: Z dimension (board thickness) divided by the hole diameter.



## MECHANICAL FABRICATION

#### PLATED HOLES

Diameters of plated holes (vias) are typically specified after plating. Obviously, plating closes down the hole diameter by twice the plating thickness. The tolerance on hole diameter after plating is limited by the combined tolerances of the drilling and plating processes.

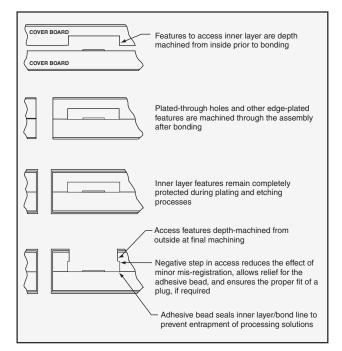
Tolerance bands (the sum of the plus and minus deviations from nominal) should be 5 mils for diameters up to 30 mils, 6 mils for diameters up to 61 mils, and 8 mils for diameters over 62 mils. Note that for aspect ratios greater than 4:1, tolerances should be increased by up to 4 mils due to "dogboning" (overplated corners due to variations in plating current distribution). Hole size, hole density, and sizes and shapes of adjacent circuits and ground planes also contribute to variability in the local current density.

When electroplated tin/lead is to be reflowed or fused, hole size should only be specified prior to reflow. During the reflow operation, the individual part design, including pad size, hole size, material thickness, and trace thickness, will influence the amount of solder flow and any dimensional measurement subsequent to that process.

#### INNER LAYER ACCESS MACHINING

Since the best and most cost efficient means for fabricating bonded assembly access cavities requires two precision depth cuts one from each side of each cover board, it is useful to permit a step in the access sidewall of up to 20 mils. This step is usually the result of oversizing the portion of the access closer to the circuit layer, although this step can be made in the other direction as well, see Figure 20.

# *Figure 20. Cross-sectional view of an inner layer access cavity.*





## CHEMICAL FABRICATION

#### ETCHING

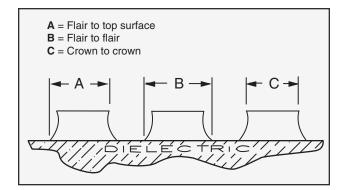
Final manufacturing tolerances are the sum of individual imaging and etching tolerances. Some general guidelines include:

- Small gaps are generally more difficult to image and etch than small lines. Manufacturability is typically reduced for lines/gaps less than 4 mils.
- It is extremely difficult to etch lines finer than twice the total copper thickness, including any panel plating thickness. Usually, it is best to minimize the required copper foil cladding thickness.
- Isolated fine lines with large associated gaps are easier to etch than clusters of fine lines grouped together.
- Sharp corners where lines change direction are more difficult to image and etch than more gradual curved or 45° mitred corners.

During etching, the lateral undercutting of copper under the photoresist reduces line widths and increases gap widths by an amount proportional to the total copper thickness (including any panel plating) which must be removed to define the circuitry. The etch factor predicts the amount of undercutting that will occur, and is typically equal to the etched copper thickness when dryfilm resists are employed. Thus, as a general rule, line widths shrink, and gaps grow, by the etched copper thickness.

When metals such as gold or tin/lead are used as an etch resist, copper undercutting is more pronounced due to the galvanic attack of copper in contact with the more noble metal resist. When compared with dryfilm resist, etch factors can be up to 100% larger for gold resist and 50% larger for tin/lead resist. When specifying etched feature tolerances, it should be clearly stated exactly where on the trace the measurements should be taken. Most often printed circuit board manufacturers are instructed to measure trace widths at the base of the circuit trace, or flare (see Figure 21). Alternately, they can provide measurements from crown to crown, although this method is less accurate due to the subjective nature of determining exactly where the trace crown is. Most optical measurement equipment provides crown to crown measurements.

*Figure 21.* Alternate Methods of Specification and Measurement of Line Widths.



## CHEMICAL FABRICATION

# METALIZATION & COPPER PLATING

Before electrolytic plating, all exposed dielectric (e.g., thru-holes barrels, edges) and metal surfaces are covered with very thin (typically 30 microinches) of electroless copper. Copper is then electroplated to the required thickness in either a panel or pattern plate scheme.

As a general rule, panel plating is preferred where plating thickness uniformity is most critical, since the image does not influence the plating distribution. In addition, where thick metalization is required, panel plating permits heavy buildup of metal without bridging over the plating resist film. In contrast, pattern plating is preferred when line/gap tolerances are most critical, since the thickness and uniformity of copper to be etched away is dictated only by the copper cladding.

The mechanical properties of plated copper determine the thermal shock resistance and thermal cycle resistance to plated-thru hole cracking during soldering and assembly, as well as during the environmental thermal cycling that often occurs in service. Copper must be ductile enough to withstand the high thermo-mechanical stresses generated at soldering temperatures, but also strong enough to resist fatigue failures resulting from smaller environmental thermo-mechanical stresses. Percent elongation should be 20% and 40 Kpsi tensile minimum for annealed copper foils tested per ASTM test methods.

Plating thicknesses are usually specified as minimum thickness in the plated thru-hole barrel. Class 1 is recommended (Figure 22). Frequently, print specifications will require a minimum of 1 mil of copper on the sidewall of a plated thru-hole. In such cases, printed circuit board companies would target their plating at 1.5 mils. Plating thicknesses greater than 2 mils can create problems with etch precision. Tight bilateral copper plating tolerances are often difficult to achieve. This is because the hole pattern will affect metal distribution for panel plating (isolated holes will plate faster than densely-packed holes) and the plating (the area density of the plated image) will dictate metal thickness uniformity across the part. Tolerances should be broader for parts where hole patterns or images are not uniformly distributed. Plated copper thickness tolerances of  $\pm 0.5$  mils are typical, and manufacturability will be reduced (depending on the part) for tolerances of  $\pm 0.2$ mils or less.

| Figure 22.   | Copper Plating Thicknesses as |  |
|--------------|-------------------------------|--|
| Specified in | <i>MIL-C-14550A</i> .         |  |

| Class | Thickness           |  |
|-------|---------------------|--|
| 0     | 1.0 mil to 5.0 mils |  |
| 1     | 1.0 mil minimum     |  |
| 2     | 0.5 mil minimum     |  |
| 3     | 0.2 mil minimum     |  |
| 4     | 0.1 mil minimum     |  |

When total metalization or overall part thickness is specified, tolerances should reflect the sum of the plating tolerance, plus the copper foil thickness and/or dielectric thickness tolerances. Copper foil thickness is specified by copper weight per unit area (oz/sq. ft.) in IPC-CF-1500E. Plating and total metalization thickness is evaluated by microscopic examination of polished and etched microsections. Internal coupons plated in areas later cut out of the circuit give the best indication of part thickness. Otherwise, coupons are added to the border area, or parts are destructively tested.

## CHEMICAL FABRICATION

### THICK AND THIN TIN/LEAD PLATING

Thick tin/lead plating is governed by MIL-P-81728A. The tin/lead deposit must be 50-70% tin. Walls of holes such as plated thru-holes in printed circuit boards should have a plating with a "minimum average of 300 microinches of thickness. No single measurement shall be less than 200 microinches of minimum thickness."

Tin/lead plating thickness and composition is verified by non-destructive X-ray fluorescence measurements at four locations on parts selected from each tankload. Measurement accuracy is better than  $\pm 10$  microinches. If required, plated tin/lead is fused by immersion in hot oil after cleaning and fluxing.

Tin/lead as an alloy is called solder. You may see some references to pretreating a copper surface with solder as "tinning" or "pretinning" a surface to make it solderable. This usually is a reference to solder dipping, not just tin.

Thin solder deposits are traditionally created by dipping the board into molten solder for coverage The molten solder sticks to the clean copper surfaces it encounters and does not adhere to the plastic surfaces. When the board is withdrawn from the liquid solder, it is "blown off" by a blast of air forced through an air-knife onto the surface of the board, leveling the solder deposit. This process is called hot air solder leveled or HASL. Without blowing off the solder, basic rules of physics take over (surface tension) and the tin/ lead deposits will have severe variation in thickness depending on gravity, cooling rates, and the surface characteristics of the copper undersurface. HASL thicknesses are typically 80 millionths of an inch.

Fused solder is superior to "as-plated" tin/lead for solderability characteristics over time. Recent environmental initiatives have led printed circuit board designers to reconsider the use of tin/lead on board surfaces for solderability. For cost, performance, and environmental considerations, the preferred choice today is electroless nickel/immersion gold.

| 🖺 Connector Connections.txt   | ×       |
|---|---------|
| <u>F</u> ile <u>E</u> dit <u>S</u> earch <u>H</u> elp   |         |
| Trompeter leaded PCB components<br>may be ordered with solder<br>dipped legs to ensure highly<br>solderable interconnects<br>without danger of cold<br>solder joints. | 4       |
|   | -<br>// |

## ELECTROLESS NICKEL/ IMMERSION GOLD

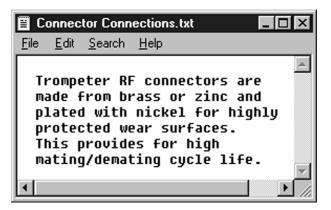
Some printed circuit board companies provide an electroless nickel/immersion gold plating finish for parts that undergo surface mount component operations. This finish is superior to many traditional tin/lead plating schemes due to the reasons previously mentioned (see tin/lead section), plus superior flatness and resistance to oxidation. The immersion gold plating thickness is only 4 to 7 microinches, serving to prevent oxidation of the nickel, and not contributing to solder joint embrittlement as thicker electroplated gold does. The electroless nickel thickness is typically 100-180 millionths of an inch. Due to the nature of the process, the plating is highly uniform and has excellent coverage.

| 🖺 Co           | nnec                 | tor Conn                   | ection               | s.txt 📃 🗶  |
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| <u>F</u> ile   | <u>E</u> dit         | <u>S</u> earch             | <u>H</u> elp         |  |
| co<br>el<br>go | nnec<br>ectr<br>1d p | tor, to<br>oless<br>olated | the U<br>nick<br>for | rface mount RF<br>CBJE20 is<br>el immersion<br>high solderability<br>reflow lines. |

## CHEMICAL FABRICATION

#### NICKEL PLATING

On most printed circuit boards electroplated nickel is usually plated in conjunction with electroplated gold. Nickel is specified wherever a uniform, hard and highly corrosion resistant coating is required. Its high harness and lack of porosity make it an ideal barrier coating for components exposed to aggressive usage or hostile environments. Nickel plating for engineering purposes is used for wear resistance, abrasion resistance and such incidental corrosion protection of parts as specified thickness of the nickel plating may afford.



*Figure 23.* Specifications For *Electrodeposited Gold per MIL-G-45204C.* 

#### GOLD PLATING

The specifications for electrodeposited gold are governed by MIL-G-45204B and include Type (minimum gold purity), Grade, (Knoop hardness) and Class (minimum thickness). The mechanism of gold hardening typically involves co-deposition of other metals, so some gold types prelude certain hardness grades.

Thickness can be measured non-destructively using an X-ray fluorescence technique, and is accurate to better than  $\pm 5$  microinches. Thickness of 50 microinches (Class 1) is specified for most applications. Thinner coatings are not usually recommended since coverage may be incomplete, and microporosity may result in localized accelerated galvanic corrosion. In addition, gold overhang, always generated when gold etch resists are used, is less likely to cause slivers that can result in shorts. As with any pattern plating, the image distribution will dictate metal distribution, which is extremely difficult to predict, so tolerances of  $\pm 20\%$  of nominal thickness are recommended.

| Туре  | Purity                     |      | lardness<br>les Included |
|-------|----------------------------|------|--------------------------|
| 1     | 99.7% minimum              | A    | A, B, or C               |
| 11    | 99.0% minimum              | E    | B, C, or D               |
|       | 99.9% minimum              |      | A only                   |
| Grade | Knoop<br>Hardness          |      |                          |
| Α     | 90 maximum                 |      |                          |
| В     | 91 - 129                   |      |                          |
| С     | 130 - 200                  |      |                          |
| D     | 201 and over               |      |                          |
| Class | Deposit Thick<br>(Minimum) | ness |                          |
| 00    | 20.0 microinch             | es   |                          |
| 0     | 30.0 microinch             | es   |                          |
| 1     | 50.0 microinch             | es   |                          |
| 2     | 100.0 microinc             | hes  |                          |
| 3     | 200.0 microinc             | hes  |                          |
| 4     | 300.0 microinc             | hes  |                          |
| 5     | 500.0 microinc             | hes  |                          |
| 6     | 1500.0 microinches         |      |                          |

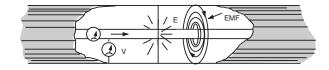


## CHEMICAL FABRICATION

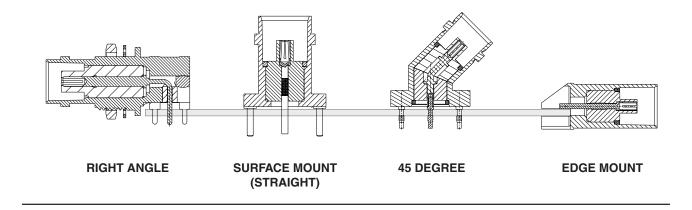
| FINISHES   | BENEFITS  | CONCERNS/LIMITATIONS  | SUGGESTED<br>THICKNESS                     |
|--|---|---|--|
| <b>ELECTROLYTIC</b><br><b>COPPER</b><br>Good electrical conductor. Good<br>elongation and ductility. Low cost, easily<br>soldered. Acid copper is industry<br>standard. Antitarnish can be applied for a<br>reasonable shelf life. Flat surface. |   | Unprotected surface will have a layer of copper oxide. Best with a top coat of other plated metal.  | 1000 - 2000<br>Microinches                 |
| TIN/LEAD         Low cost, good solderability, good etch resist for alkaline etchants. Flat surface.   |   | Lead is toxic, a carcinogen and a<br>reproductive toxin. Exposed copper will<br>exist along circuit edges. Requires<br>copper underplate on brass substrates.<br>Has overhang silvers. Limited shelf life<br>due to oxidation and uneven cosmetics. | 300 - 500<br>Microinches                   |
| TIN/LEAD FUSED<br>(SOLDER)         Industry standard.           Low cost, very solderable,<br>good shelf life.         good shelf life.  |   | Lead is toxic, a carcinogen and a<br>reproductive toxin. Non-uniform surface,<br>poor for surface mount components.   | As<br>Plated                               |
| HOT AIR SOLDER<br>LEVEL (HASL)   | Covers sides of copper circuits. Makes<br>more uniform alloy of Pb/Sn than when<br>plated. Less chance of dewetted<br>solder. Used when soldermask over<br>bare copper is required.                                       | Can produce brittle alloy at copper to Pb/Sn<br>interface when thin (intermetalics).<br>Thickness depends on circuit geometry or<br>soldermask land configuration. Front to<br>back thickness variation.  | 100 - 500<br>Microinches                   |
| HYDROSQUEEGY<br>(HOT OIL LEVEL)         Flattest solder surface profile.<br>Uniform sidewall coverage.   |   | Coating too thin (<0.1 mil) which impacts solderability and shelf life.   | < 100<br>Microinches                       |
| ELECTROLESS<br>NICKEL/IMMERSION<br>GOLD  | Deposits are of uniform thickness.<br>Provides complete coverage of isolated<br>circuit traces. Low surface resistance.<br>Easily soldered. Thin gold coating<br>does not embrittle solder. Good<br>oxidation resistance. | Nickel is a carcinogen. Maximum gold<br>thickness is approximately 8 microinches.<br>Requires removal of photoresist films<br>before application.   | Ni - 50 - 150<br>Au - 2 - 8<br>Microinches |
| ELECTROLYTIC<br>NICKEL<br>Diffusion barrier between copper and<br>gold plating. Improves wear resistance<br>of gold when used as under plate.<br>High temperature resistance if<br>sulfur free.  |   | Carcinogen. Not easily soldered.<br>Deposits can be highly stressed or brittle<br>if solution does not have proper controls.<br>Oxidation an issue if not protected.  | 100 - 200<br>Microinches                   |
| <b>ELECTROLYTIC</b><br><b>GOLD (TYPE 2 &amp; 3)</b> Gold electrical conductor. Lowest<br>surface resistance. Good corrosion<br>resistance. Surface free of oxide films.<br>Resists all etchants.   |   | High cost. Very soluble in solder, can<br>cause embrittlement of soldered<br>connections. Promotes undercut when<br>used as etch resist. Accelerates<br>corrosion of base metal unless pore free.   | 50 - 100<br>Microinches                    |
| IMMERSION TIN Uniform deposit thickness. Easily soldered. Will provide complete coverage of isolated circuit traces.   |   | Maximum deposit thickness of<br>approximately 70 microinches. Has<br>limited shelf life. Very susceptable to<br>oxidation/environment.  | 25 - 50<br>Microinches                     |

## Figure 24. Typical Metal Characteristics.

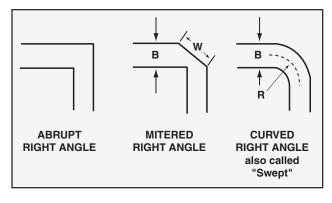
Remember ... the first rule of connectors is that the best connector is no connector at all...



EMF = Electromagnetic Field E = Energy V = Voltage

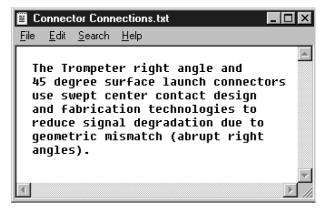


Handling the Corner, Etching Versus Physically Bent



B = Conductor Width R = Radius W = Mitred Edge

At high frequency, abrupt right angles create geometric reflections that show up as return loss. Since the planarity of a printed circuit board has the signal and ground traces running in the X and Y direction with interconnecting vias (plated thru holes) in the Z direction, there is significant opportunity for return loss. This is even more true when the signal enters a launching RF connector jack, (see above) in all but the Edge Mount case. Note that the Edge Mount connector is a surface mount connector and features the center pin in line with the planarity of the board.



|   |           | <u>series</u> |                                    | SURFACE MOUL<br>(STRAIGHT)                                  | NT                               | 45 DEGREE |             | RIGHT ANGLE  |  | EDGE<br>MOUNT |      |
|---|-----------|---------------|------------------------------------|---|----------------------------------|-----------|-------------|--|--|---------------|------|
|   |           |               | <u>BULKHEAD</u><br><u>MOUNTING</u> |   | <u>Page</u>                      |           | <u>Page</u> |  | Page                                   |               | Page |
|   | JRE       | BNC           | Non-Bulkhead                       | CBJ20<br>CBJ22<br>UCBJ223<br>UCBJ224<br>UCBJ20F<br>105-1829 | 20<br>20<br>20<br>20<br>21<br>21 | 105-2033  | 21          | (U)CBJR220<br>(U)CBJR20A<br>(U)CBJR20  | 22<br>22<br>22                         | UCBJE20       | 23   |
|   | ΜΙΝΙΑΤU   | BNC           | Bulkhead                           | (U)CBBJ26GF<br>CBBJ26<br>UCBBJ23                            | 24<br>24<br>24                   |           |             | (U)CBBJR229<br>(U)CBBJR29/A<br>(U)CBBJR26/A                                  | 25<br>25<br>25                         | UCBBJE20      | 23   |
|   | Σ         | TNC           | Non-Bulkhead                       | (U)CBJ40  | 26                               |           |             | (U)CBJR40/A  | 27                                     |               |      |
| LRE   |           | TNC           | Bulkhead                           | (U)CBBJ46GF   | 26                               |           |             | (U)CBBJR49/A<br>(U)CBBJR46/A<br>105-1742                                     | 27<br>27<br>27                         |               |      |
| SUB-MINIATURE   |           | TPS           | Non-Bulkhead                       | CBJ50(FL)<br>CBJ350   | 28<br>31                         |           |             | CBJR50(FL)<br>CBJR350<br>105-1837  | 28<br>28<br>28                         |               |      |
| in in iteration in the second |           | F             | Non-Bulkhead                       | CBJ130L   | 29                               |           |             |  |  | CBJE130       | 29   |
| Ŋ   |           | F             | Bulkhead                           | 105-1839<br>CBBJ139   | 29<br>29                         |           |             |  |  |               |      |
|   |           | MINI-WEC      | Co                                 | CBJ12   | 30                               |           |             | CBJR12<br>CBJR12A<br>CBBJR12<br>CBBJR12A<br>105-1880                         | 30<br>30<br>30<br>30<br>30<br>30       |               |      |
|   |           | Ν             | Bulkhead                           |   |                                  |           |             | CBBJR99  | 21                                     |               |      |
|   |           | TRB           | Non-Bulkhead                       | CBJ70(TL)(FL)   | 31                               |           |             | CBJR70/A   | 31                                     |               |      |
|   | MINIATURE | TOT           | Bulkhead                           | CBBJ74<br>CBBJ79(TL)(FL)                                    | 31<br>31                         |           |             | CBBJR74/A<br>CBBJR79/A<br>305-0789<br>305-0848<br>CBBJR74FL/A<br>CBBJR74FL/A | 32<br>33<br>33<br>33<br>32<br>32<br>32 |               |      |
| Ш   |           | TRT           |                                    | CBBJ379<br>CBBJ374/A  | 32<br>32                         |           |             | CBBJR379/A   | 33                                     |               |      |
| SUB-MINIATURE   |           | TRS           |                                    | CBJ157(FL)<br>CBJ3157<br>305-1174<br>305-1128               | 34<br>34<br>34<br>34             |           |             | 305-1259<br>CBJR157(FL)<br>305-0723<br>CBBJ159<br>CBBJR159                   | 35<br>35<br>35<br>35<br>35<br>35       |               |      |
| Ω<br>Ω  |           | Specials      |                                    | CBBJ82 (TRC)<br>CBBJ823<br>305-0896<br>CBSPC8P              | 36<br>36<br>36<br>36             |           |             | CBBJR39A   | 36                                     |               |      |

GDAXIAL

WINAX / TRIAX

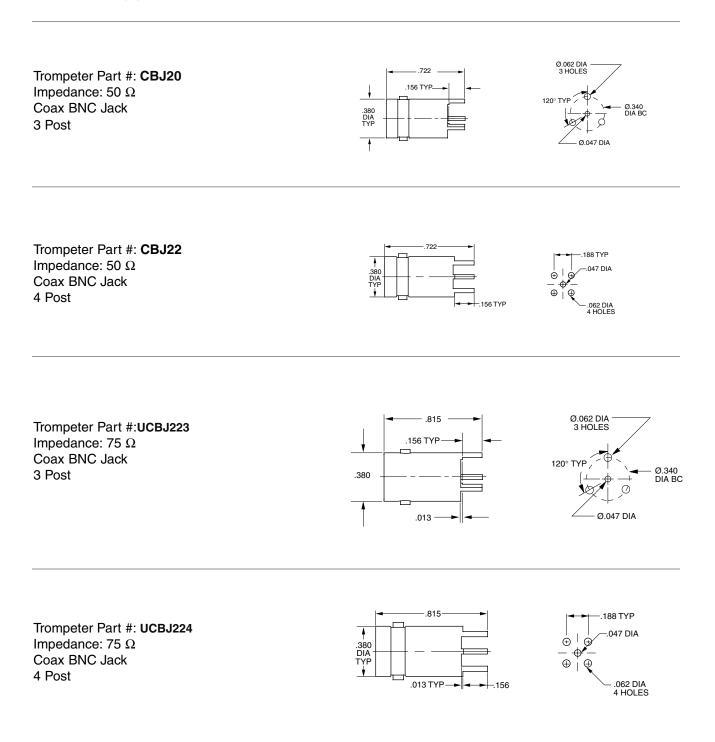
#### 20

# SECTION III - PCB MOUNTED RF CONNECTORS

## COAX

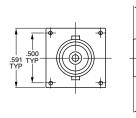
Trompeter's line of BNC and TNC jacks are available in 50 and 75  $\Omega$  impedance. These are not just 50 or 75  $\Omega$  interfaces, they have *true impedance* throughout the connectors.

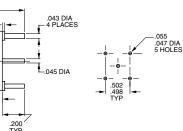
These straight BNC jacks, CBJ20 and CBJ22 are of machined half-hard brass bodies and available in 50  $\Omega$  or 75  $\Omega$  (U) versions.



## COAX

Trompeter Part #: **UCBJ20F** (Also known as 105-1478) Impedance: 75  $\Omega$ Coax BNC Surface Mount 4 Post





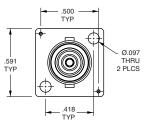
(.850)

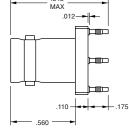
.013 TYP

.090 - 560.

Trompeter Part #:105-1829 Impedance: 75  $\Omega$ Coax BNC Surface Mount Compliant Tail & Mount Legs

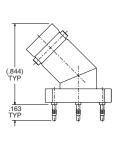
Leg Length to Accommodate .090 - .130 Board

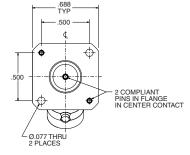




845

Trompeter Part #:. **105-2033** Impedance: 75  $\Omega$ Coax BNC Surface Mount 45 degree with Compliant Tail Contact & Mount Legs



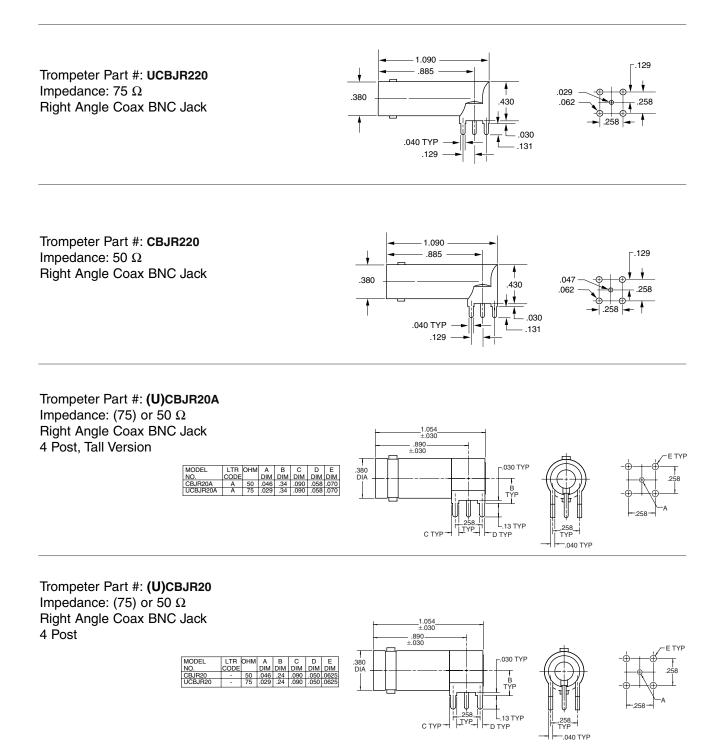


Trompeter Part #:CBBJR99 .867 Impedance: 50  $\Omega$ .555 Coax Type "N" Right Angle 930 - 187 .450 TYP **Bulkhead Mount** .553 350 .866 DIA Maximum Panel Thickness: .310 D Hole: DD4 .800 DIA .750 HEX NUT-.072 .104 DIA 4 PLACES

See the Appendix for more information on compliant pin technology.

## COAX

All Trompeter BNC printed circuit board connectors are machined brass bodies for high strength excellent durability, long mating life.



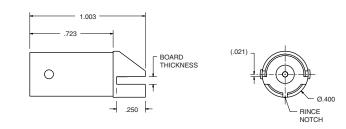
### COAX

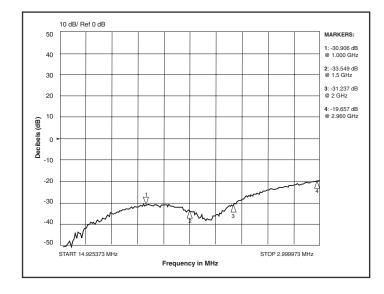
Trompeter Part #: **UCBJE20** Impedance: 75  $\Omega$ Circuit Board Edge Mount Coax "BNC" Style Receptacle

 Part #:
 Board Thickness

 UCBJE20-1
 .060-.064

 UCBJE20-2
 .028-.033

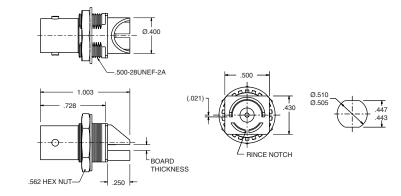




Trompeter Part #:**UCBBJE20** Impedance: 75  $\Omega$ Circuit Board Bulkhead Edge Mount Coax "BNC" Style Receptacle

Max Panel Thickness: .179

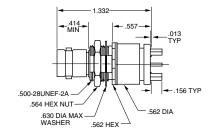
| <u>Part #:</u> | <b>Board Thickness</b> |
|----------------|------------------------|
| UCBBJE20-1     | .060064                |
| UCBBJE20-2     | .028033                |
| UCBBJE20-3     | .084                   |

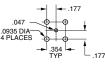


## COAX

Trompeter Part #:**CBBJ26** Impedance: 50 Ω Coax BNC Insulated 4 Post Bulkhead Mount

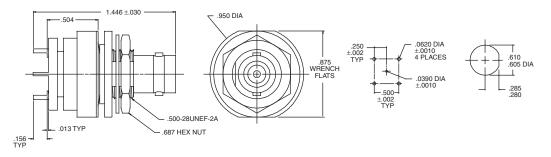
Maximum Panel Thickness: .156 D Hole: D3





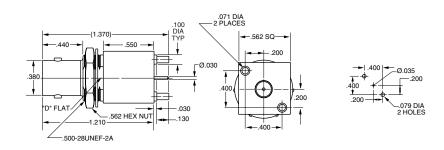
Trompeter Part #: **(U)CBBJ26GF** Impedance: (75) or 50  $\Omega$ Coax BNC Ground Filter Insulated 4 Post Bulkhead Mount

Maximum Panel Thickness: .093 D Hole: D8



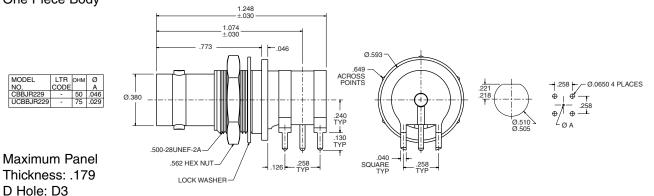
Trompeter Part #:**UCBBJ23** Impedance: 75  $\Omega$ Coax BNC Rear Mounting Non-Insulated Bulkhead Mount

Maximum Panel Thickness: .060 D Hole: D3



### COAX

#### Trompeter Part #: (U)CBBJR229 Impedance: (75) or 50 $\Omega$ Coax BNC Right Angle Bulkhead Mount One Piece Body



See Appendix for performance test data.

Trompeter Part #: (U)CBBJR29/A Impedance: (75) or 50  $\Omega$ Coax BNC Right Angle Bulkhead Mount

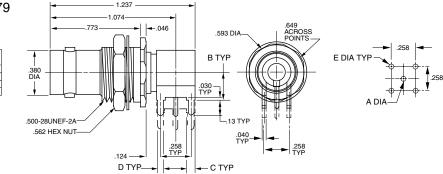
Maximum Panel Thickness: .179 D Hole: D3

LTB OHM

MODEL

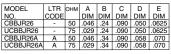
CBBJR29 UCBBJR29 A B DIM DIM

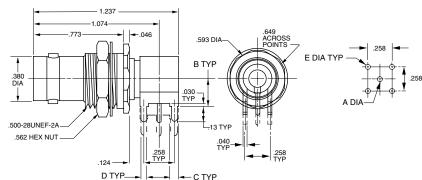
C D



Trompeter Part #: (U)CBBJR26/A Impedance: (75) or 50  $\Omega$ Coax BNC Insulated Right Angle Bulkhead Mount

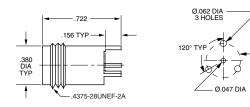
Maximum Panel Thickness: .156 D Hole: D3





## COAX

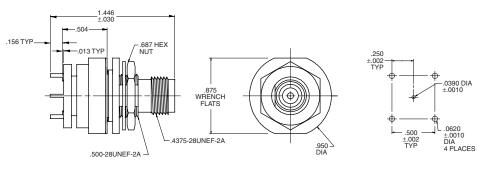
Trompeter Part #:(U)CBJ40 Impedance: (75) or 50  $\Omega$ Coax TNC Surface Mount 3 Post



Ø.340 DIA BC

Trompeter Part #:(U)CBBJ46GF Impedance: (75) or 50  $\Omega$ Coax TNC Bulkhead Mount with Ground Filter

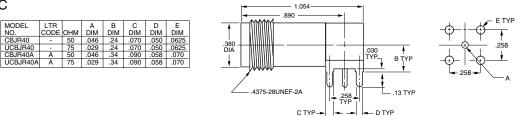
Maximum Panel Thickness: .093 D Hole: D8





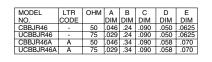
## COAX

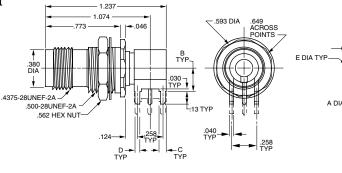
Trompeter Part #:(U)CBJR40/A Impedance: (75) or 50  $\Omega$ **Right Angle Coax TNC** 



- D TYP

Trompeter Part #:(U)CBBJR46/A Impedance: (75) or 50  $\Omega$ Right Angle Coax TNC Bulkhead Mount



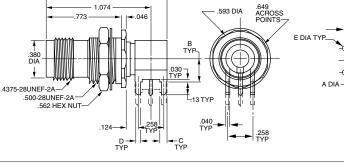


1.237

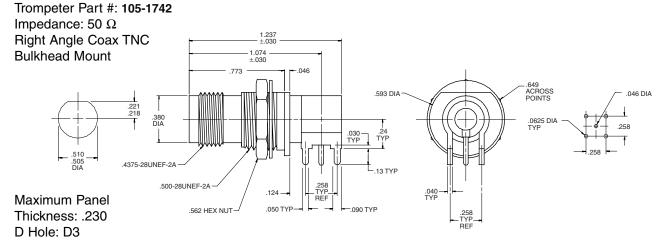
Maximum Panel Thickness: .156 D Hole: D3

Trompeter Part #: (U)CBBJR49/A Impedance: (75) or 50  $\Omega$ Right Angle Coax TNC Bulkhead Mount

| MODEL     | LTR  | OHM | A    | В   | С    | D    | E     |
|-----------|------|-----|------|-----|------|------|-------|
| NO.       | CODE |     | DIM  | DIM | DIM  | DIM  | DIM   |
| CBBJR49   | -    | 50  | .046 | .24 | .090 | .050 | .0625 |
| UCBBJR49  | -    | 75  | .029 | .24 | .090 | .050 | .0625 |
| CBBJR49A  | A    | 50  | .046 | .34 | .090 | .058 | .070  |
| UCBBJR49A | Α    | 75  | .029 | .34 | .090 | .058 | .070  |



Maximum Panel Thickness: .230 D Hole: D3

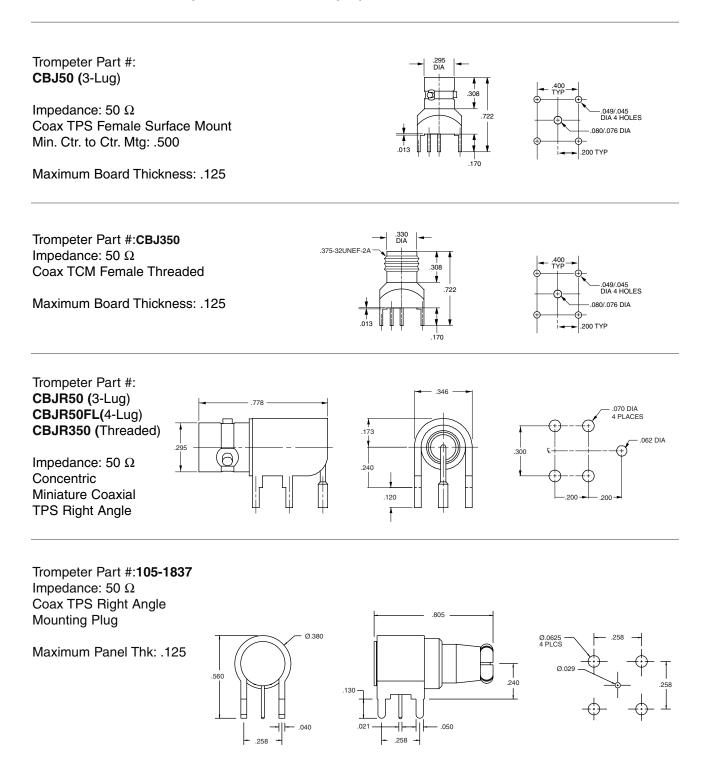


258

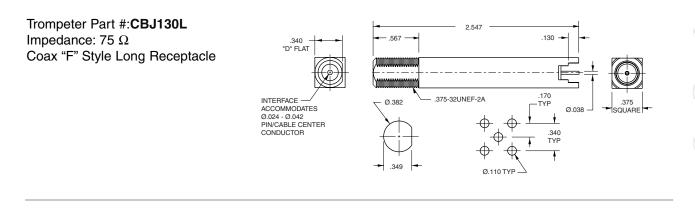
27

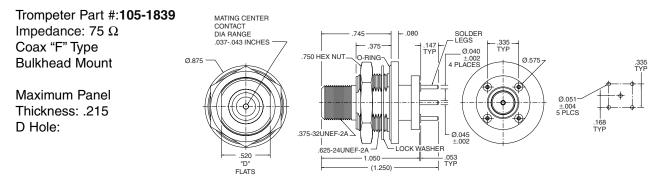
## COAX

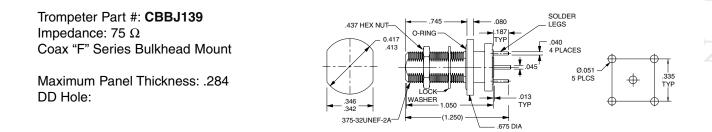
TPS connectors are for applications that require coax connectors but do not have space for standard BNC connectors. TPS connections are 33% smaller than standard BNC connectors' allowing more connections per board. TPS connectors offer the same electrical characteristics as standard BNC products and are offered in both bayonet and threaded body styles.



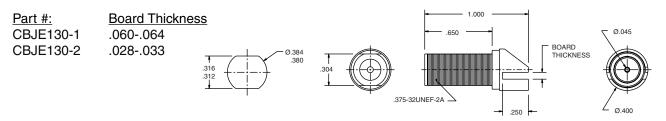
## COAX



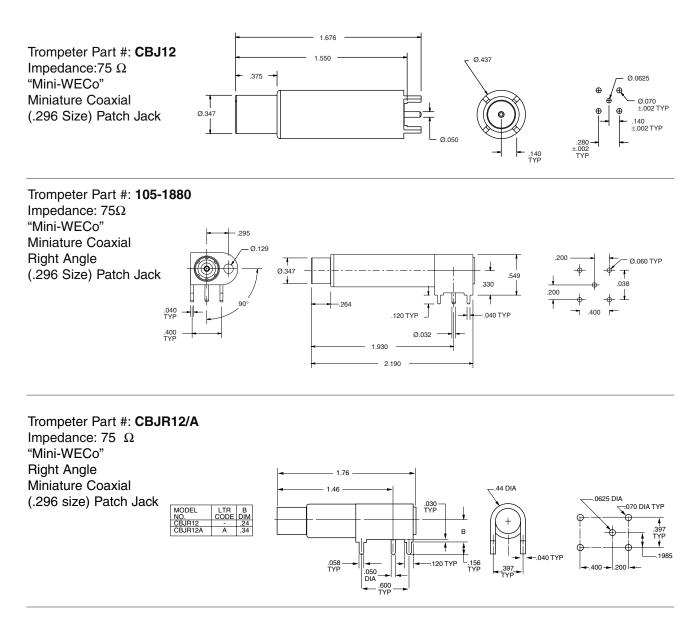




Trompeter Part #:**CBJE130** Impedance: 75 Ω Coax "F" Style Female Jack Circuit Board Edge Mount

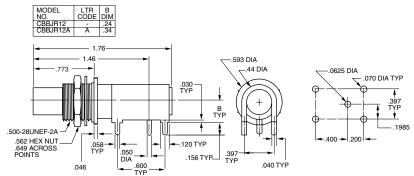


## COAX



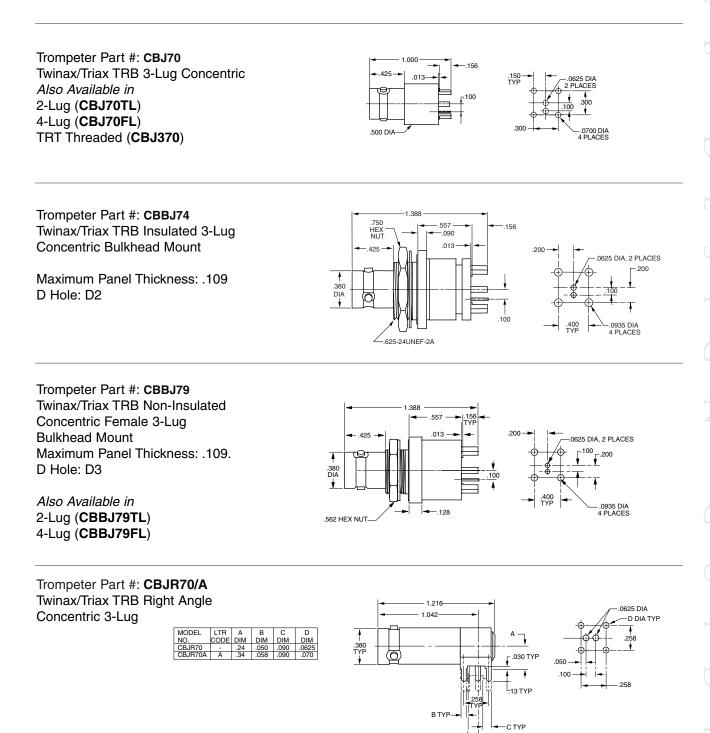


D Hole: D3



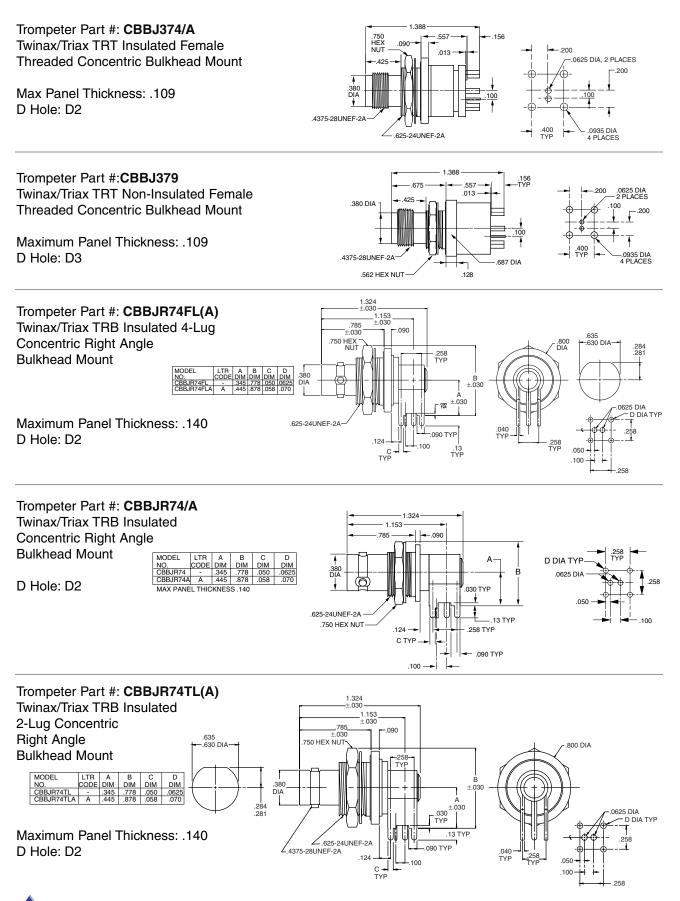
## TWINAX/TRIAX

TRB is the acronym for concentric Triax Bayonet Connectors and TRT is Triax Threaded Connectors. TRB and TRT connectors are manufactured per Mil-C-49142 and are used in the interconnection of Twinax or Triax cables. Twinax applications for these connectors include twisted shielded pair connections for commercial and military 1553 Data Bus and shielded video. These connections offer three shielded connections for a main conductor and two shields.



.100

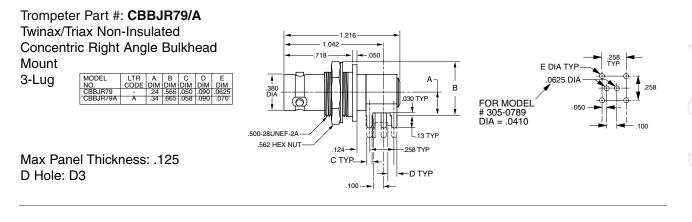
## TWINAX/TRIAX

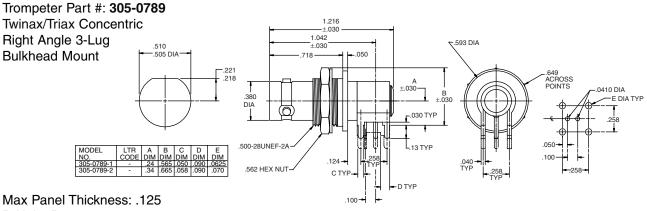


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## TWINAX/TRIAX





1.216 ±.030

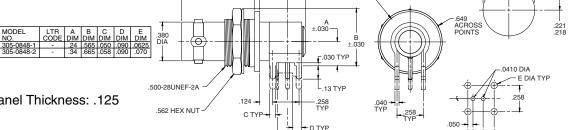
1.042 ±.030

.718

D Hole: D3

Trompeter Part #: 305-0848

Twinax/Triax Concentric Right Angle Bulkhead Mount 4-Lug



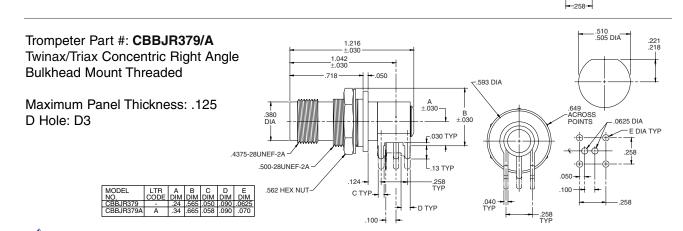
Ļ

.100 🕂

.050

-.593 DIA

Maximum Panel Thickness: .125 D Hole: D3



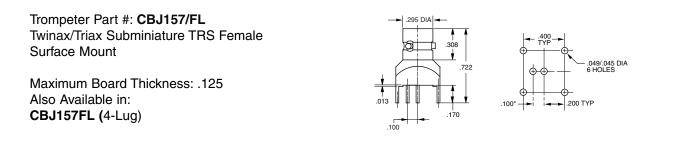
TROMPETER ELECTRONICS INC. PH: 800-982-2629 FAX: 818-706-1040 WWW.TROMPETER.COM

.510 .505 DIA

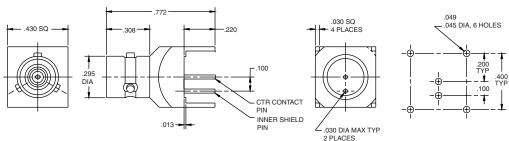
.100 -÷ ÷

## TWINAX/TRIAX

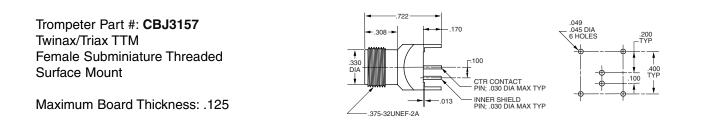
TRS connectors are for applications that require Twinax or Triax connections but do not have the available space for standard TRB connectors. TRS connections are 33% smaller than standard TRB allowing more connections per board. TRS connectors offer the same electrical characteristics as standard TRB products and are offered in both bayonet and threaded body styles.

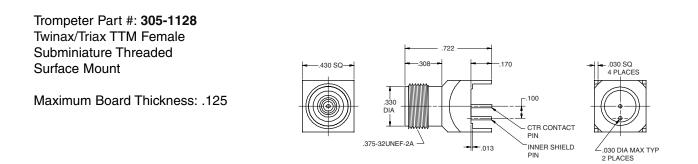


Trompeter Part #: **305-1174** Twinax/Triax Female TRS Subminiature 3-Lug Surface Mount



Max Board Thickness: .175

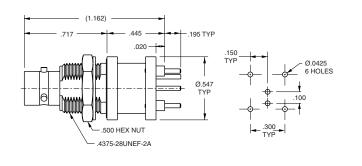




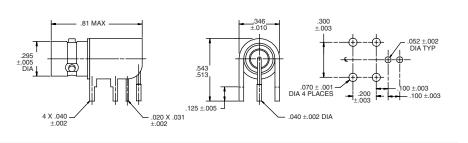
## TWINAX/TRIAX

Trompeter Part #: **CBBJ159** Twinax/Triax TRS Female Subminiature 3-Lug

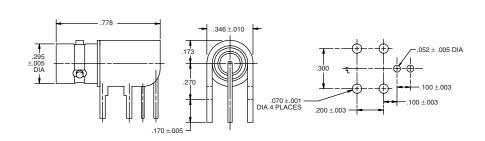
Maximum Panel Thickness: .250 D Hole: D4



Trompeter Part #: **305-1259** Twinax/Triax TRS Right Angle Female Subminiature 3-Lug

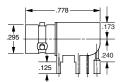


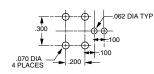
Trompeter Part #: **305-0723** Twinax/Triax TRS Right Angle Female Subminiature 3-Lug



Trompeter Part #: **CBJR157/FL** Twinax/Triax TRS Subminiature Right Angle

Also Available in: CBJR157FL (4-Lug) CBJR3157 (TTM-Threaded Version)





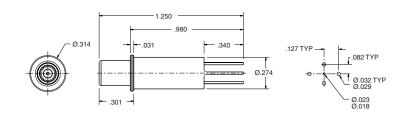
Ø.070±.001

100± .003

.562 HEX Trompeter Part #: CBBJR159 .665 ±.030 Twinax/Triax TRS Subminiature Right Angle Bulkhead Rear Mount 3-Lug +.030 Maximum Panel Thickness: .125 4375-28UNEF-2A Ø.062±.001 .500 HEX NUT D Hole: D4 L<sub>.120</sub> 125 Ø 443 100+.003

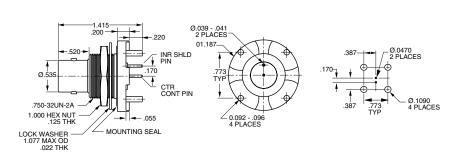
#### SPECIAL APPLICATION SAMPLES

Trompeter Part #: **CBSPC8P** Circuit Board Mount Scoop Proof Contact Size 8 Pin for MIL-C-38999 Series 1,3,4 Multi-pin Connectors.



Trompeter Part **# CBBJ82(3)** TRC Bulkhead Mount 3-Lug Version (CBBJ823)

Maximum Panel Thickness .250 Maximum Board Thickness: .190

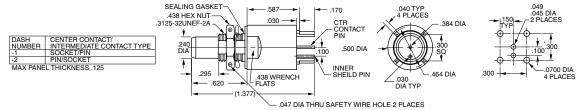


Trompeter Part #: **305-0896** Concentric Twinax Non-Insulated Bulkhead Mount, with Pin or Socket Intermediate

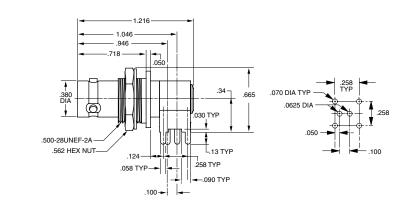
Contact or Socket or Pin Center Contact.

Meets the outgassing requirements for Nasa Specification SP-R-0022.

Max Panel Thk .125



Trompeter Part #: **CBBJR39A** Right Angle Two-Pin Polarized Twinax (TWBNC)



Maximum Panel Thickness: .125 D Hole: D3

## HIGH VOLUME PLACEMENT

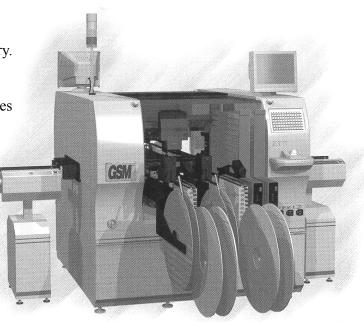
## **Edge Connector Feeder**

Universal offers an edge connector feeder for handling large, unique components such as SIMMs, DIMMS, and automotive componentry.

## **GPAX Tape Feeders**

GPAX tape feeders handle deep or wide devices packaged in GPAX-style tape from 56mm to 200mm wide. Both semipocket tape and flat carrier type formats are available.





**Photo:** Universal's GSM2-Connector offers fast, automatic insertion of a wide range of thru-hole and surface mount odd form components.

#### COMPLIANT PIN TECHNOLOGY

RF connectors as components on circuit boards almost always have some sort of leg or thru-hole construction to manage the torque requirement that is associated with such connectors. Soldering to pads alone (surface mount) depends on the adhesion characteristics of the copper foil to the board substrate, usually something in the range of 10 pounds per square inch, and dependent on the footprint of the pad itself.

Pin technology solves the torque issues but usually mandates a secondary soldering requirement due to soldering leads and/or the soldering issues that surround a thermal mass like an RF connector as opposed to an SMT component.

An alternative technology to soldering is press fit. Within the press fit option, there are two choices: 1. A solid pin that does not deform in the insertion process. This is usually an interference fit where considerable Z-direction forces are imparted to the hole sidewalls. A variation on this approach is a square pin that is designed to cut into the sidewalls of the hole during insertion.

2. A compliant pin which compresses as a result of insertion into the PCB thru-hole. The use of compliant pins for press fit contacts has several important engineering advantages:

- Reduction in size of the press fit section lessens demand on the PCB thru-hole.
- Greater tolerances can be accepted for the plated drilled thru-hole.
- Lower insertion forces are required, resulting in fewer undesirable side effects.
- Multiple "press-in" cycles into the same thru-hole are possible.

# APPENDIX Electrical Testing of RF Connectors

#### EXAMPLE TEST PROCEDURE USING UCBBJR229

#### SCOPE

The purpose of this test procedure is to define the samples, procedures for testing, set-up of test equipment and fixtures necessary to test the samples. The test procedure will also describe the tests to be performed and document the results of these tests.

Testing shall be in accordance with MIL-C-39012.

#### TESTING

Group One test samples will be soldered to one end of a test fixture PC-board and a UCBBJR229 will be soldered to the other end. These PC-board test fixtures will be identified as 1 through 10, marked with their date of assembly and measured for return loss.

Group Two test samples will not be terminated and will be identified as 2-1 through 2-3. These samples will be tested first for Insulation Resistance (IR), then Dielectric Withstanding Voltage (DWV).

#### GROUP ONE

• Return Loss: The network analyzer will be calibrated to 1.5 GHz using the open, short and load described in Appendix A. Markers will be set at 500 MHz, 750 MHz, 1.00 GHz, and 1.47 GHz.

#### GROUP TWO

- Insulation Resistance: 5000 Mohms min. @ 500VDC.
- Dielectric Withstanding Voltage: up to 1000 VAC, no breakdown.

#### TEST EQUIPMENT

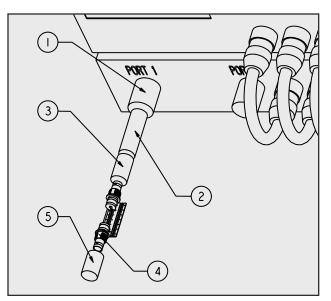
Return loss will be measured with the HP8753B Network Analyzer and HP87047A S-parameter Test Set. 'Adapter' in set-up refers to HP precision adapters.

Insulation Resistance will be measured with a General Radio Megaohm Meter model 1864.

Dielectric Withstanding Voltage will be measured with an Associated Research Hi-pot model 404.

#### TEST SET-UP

The network analyzer is set up per *Figure 25* to measure Return Loss. Terminate a 75 ohm load (selected from high precision HP test accessory kit) to one end of the article under test (see item 4 in Figure 25) and attach port 1 of the network analyzer to the other end of the article under test. Set scale to S11 (Return Loss) measurement at 10dB per division and use 0 for the reference value.



#### Figure 25.

- 1. APC-7-PL75 Adapter
- 2.  $50/75\Omega$  Min. Loss Pad
- 3. Matching Adapter
- 4. Article Under Test
- 5.  $75\Omega$  Load

## ELECTRICAL TESTING OF RF CONNECTORS

#### TEST RESULTS

See the attached spreadsheet for Return Loss (Group One samples) values at 500 MHz, 750 MHz, 1.00 GHz, and 1.47 GHz and the average Return Loss values for each connector at each frequency.

All Group Two samples passed Insulation Resistance (IR) and Dielectric Withstanding Voltage (DWV).

#### APPENDIX A: CALIBRATION

A calibration kit was created to obtain the most accurate results from the network analyzer. A load, short, and an open (see Image 2) were made from a test fixture PC-board (p/n microstrip) and a UCBBJR229 (Rev. E, lot 94989). The load has a 75 $\Omega$  resistor soldered to one end of the PC-board. The short has a copper strip soldered across the three micro-strip lines of the PC-board. The open is simply a UCBBJR229 soldered to a test fixture PC-board. The open, short, and load were made identical with equal length to the test fixture.

Calibration of the HP8753B Network Analyzer is performed in the same manner as a standard calibration with the exception of the custom calibration kit described above. H indicates a hard key on the analyzer and S indicates a soft key. Terms in the > describe the analyzer keys.

- 1. *H* <Cal>
- 2. *S* <Calibration Menu>
- 3. S <S11 1-Port>
- 4. Attach the 'open' and press S < Open >
- 5. Attach the 'short' and press *S* <Short>
- 6. Attach the 'load' and press S < Load >
- 7. *S* <Done>



*Figure 26.* Showing one end of the article under test.

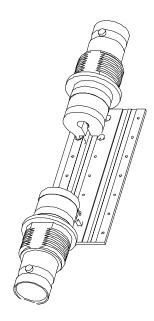


Figure 27. Article under test.

## ELECTRICAL TESTING OF RF CONNECTORS

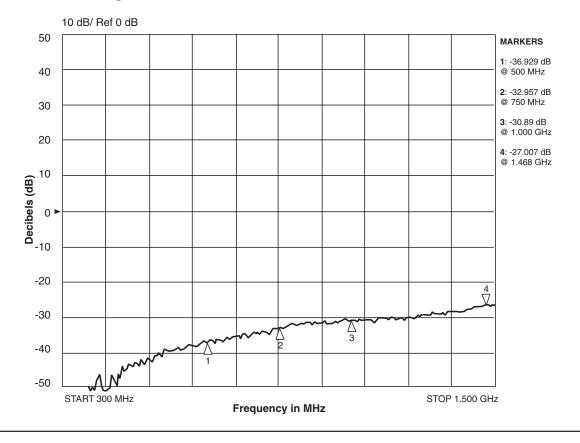


Figure 28 - UCBBJR229 Return Loss Performance

One-Piece Body UCBBJR229 Return Loss

| Test      | Return Loss (dB)<br>at: | Return Loss (dB)<br>at | Return Loss (dB)<br>at | Return Loss (dB)<br>at |
|-----------|-------------------------|------------------------|------------------------|------------------------|
| Sample    | 500MHz                  | 750MHz                 | 1.00GHz                | 1.47GHz                |
| 1         | -42.66                  | -40.48                 | -38.75                 | -35.47                 |
| 2         | -36.93                  | -32.96                 | -30.89                 | -27.01                 |
| 3         | -39.44                  | -35.95                 | -31.65                 | -26.59                 |
| 4         | -40.26                  | -36.84                 | -34.16                 | -39.38                 |
| 5         | -39.56                  | -36.79                 | -32.97                 | -26.38                 |
| 6         | -33.67                  | -32.35                 | -33.01                 | -33.38                 |
| 7         | -48.92                  | -38.39                 | -31.97                 | -29.94                 |
| 8         | -33.63                  | -30.61                 | -28.91                 | -29.63                 |
| 9         | -37.22                  | -35.19                 | -35.94                 | -34.61                 |
| 10        | -45.06                  | -44.83                 | -40.34                 | -29.46                 |
|           |                         |                        |                        |                        |
| Average   | -39.735                 | -36.439                | -33.859                | -31.185                |
| Std Devia | tion 4.830              | 4.156                  | 3.553                  | 4.354                  |

## MECHANICAL FABRICATION

#### TRUE DIMENSION TOLERANCING

Dimensioning and tolerancing of fabricated features are extremely important to printed circuit board design. On a specification of hole diameter, for example, the nominal diameter and the bilateral tolerance might be given as 0.031", +0.004" / -0.002". This example creates a tolerance band of 6 mils. Bilateral tolerancing has been in wide use for many years. This is not the case for true position tolerancing (ANSI-Y14.5M).

Manufacturability can often be improved by true position dimensioning and tolerancing which, simply stated, gives the manufacturer a tolerance budget that can be distributed between position and size in any proportion. Thus, the designer defines functionability requirements and gives the manufacturer the latitude to apply the majority of the tolerance to the least precise process.

ANSI-Y14.5M requires that a tolerance of position must specify M (maximum material condition), L (least material condition), or S (regardless of feature size). Drawings generated prior to 1982 are assumed to imply maximum material condition with respect to an individual tolerance, datum reference(s), or both where no condition is specified (Rule 2A - past practice alternate position tolerance rule).

Simply stated, maximum material condition requires that when a hole is produced at its smallest diameter (bottom of the tolerance band) the stated true position tolerances applies.

However, holes produced at larger, acceptable diameters can often be positioned with less accuracy and still provide for fit and function. Thus, for larger holes, a bonus position tolerance equal to the increase in diameter over maximum diameter is added to the true position tolerance to establish the inspection tolerance. For example, a hole of diameter  $50\pm5$  mils toleranced within a true position of 10 mils at maximum material condition implies that the net positional tolerance is 10 mils when the diameter is 45 mils: 15 mils when the diameter is 50 mils, and 20 mils when the diameter is 55 mils.

Any combination of hole diameter and net tolerance that meet the specification is acceptable.

When least material condition applies, the stated tolerance is when the hole is produced at its largest possible diameter. "Regardless of feature size" implies that the tolerance applies as stated, with no bonus tolerance and feature size tolerances based on the various process capabilities available.

Although true position dimensioning and tolerancing can apply to most conceivable features, it is most appropriate and preferred when specifying locations of holes, pockets, and other similar features where the position in both the X and Y axis together are important. Consider, for example, interconnect holes that require some minimum annular ring, or holes that may be used in assembly for tooling or component installation. The radial deviation of the hole position from nominal will dictate functionality. Since true position tolerance zones are circular (or cylindrical when considering the Z axis), they best describe the distribution of measurements that will meet assembly and performance requirements. In contrast, the square tolerance zone defined by bilateral tolerancing of the X and Y dimensions independently may exclude acceptable features, or include rejectable features, with a resulting negative impact on total system cost over the long run.

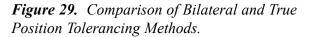
## MECHANICAL FABRICATION

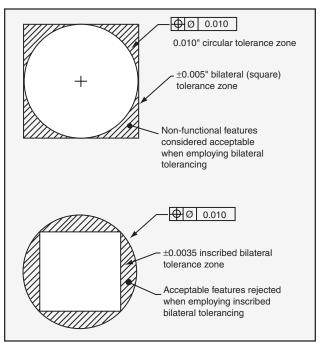
# DIMENSIONS AND TOLERANCES (CONTINUED...)

This is demonstrated in Figure 29, where we assume that the assembly process requires a radial of deviation from nominal of 5 mils or less. It is translated into a true position tolerance diameter of 10 mils. If that 5 mils radial tolerance is simply converted into a bilateral tolerance of  $\pm 5$  mils, a family of features will exist that do not meet the assembly requirements, but will be considered acceptable. In fact, over 21% of the area of the square tolerance zone includes unacceptable feature locations. The result may be excessively high rejection rates in assembly.

On the other hand, if a bilateral tolerance zone is simply inscribed in the circular tolerance zone (a very common error), an unnecessarily small bilateral tolerance is specified that excludes acceptable features. In this case, over 36% of the circular (acceptable) tolerance zone is excluded, perhaps resulting in excessive part procurement costs. (Note that the 21% and 36% "error zones" do not reflect 21% or 36% of the parts produced, since the distribution of machine error would be expected to be approximately normal, not linear.)

The use of true positive tolerancing is very useful in locating a leaded component device into a hole or a surface mount device onto a given pad.





#### TROMPETER CUSTOM PRODUCTS

The products in this design guide are your first resource for solving special interconnect problems. If you cannot find an adequate solution, then we invite you to consult us with your requirement. Visit our website at www.trompeter.com and submit one complete form for each custom product requested. Requests for new and modified products are evaluated weekly by our New Product Development Team, and you will receive a response within 3-5 working days.

## TROMPETER TRADITION OF QUALITY STANDARD AND CUSTOM COMPONENTS

For over thirty years, Trompeter's product offering has grown as a result of new and modified designs for unusual applications. Along with our tradition of custom components is a reputation for providing rugged products that have been designed to perform to expectation and to meet your specific mechanical requirements. The growth of our product line, from 78 components in 1964 to over 7,000 end items is proof of our engineering and manufacturing capabilities.

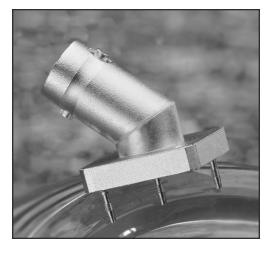
#### ISO 9001 REGISTRATION

The main value of being an ISO 9001 Registered company is in the assurance to our customers that we have a solid quality system in place, and that it is well documented. DET Norske Veritas (DNV) has certified that we are in compliance with established systems and policies. The ISO 9001 Quality System Standard is a document outlining twenty elements of quality that Trompeter addressed in order to meet registration requirements. An accredited ISO 9001 auditor must verify, through on-site audits, that a company has a well documented quality system in place that meets the requirements of ISO 9001, and that the company is working in accordance with the documented system. Trompeter's Quality System is also certified by numerous other accredited agencies.

#### TROMPETER 3-YEAR WARRANTY

All products in this catalogue carry a "Three Year Warranty" and meet or exceed the highest industrial and government standards such as MIL-C-39012 and MIL-C-49142. More importantly Trompeter connectors are designed to meet your design specification,s which are often more demanding and less forgiving. We have built our business on responding to special needs for uncompromising performance in a world of increasing pressure from global competition. If, within three (3) years of shipment, any of our products fail to meet your expectations due to defects in material or workmanship, we will gladly repair or replace it free of charge.

## NOMENCLATURE - TROMPETER PART NUMBERS



# FAMILY (SERIES)

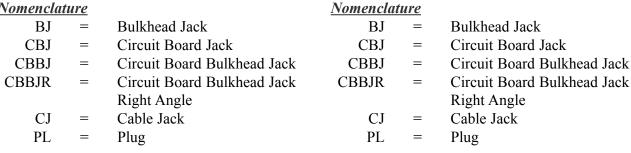
## **COAX**

| 20  | = | Wrench Crimp BNC |
|-----|---|------------------|
| 220 | = | Tool Crimp BNC   |
| 40  | = | Wrench Crimp TNC |
| 240 | = | Tool Crimp TNC   |
| 50  | = | Wrench Crimp TPS |
| 350 | = | Threaded TPS     |
| 90  | = | N Connector      |
| 130 | = | F Connector      |

## COAX PREFIX

Blank = 50 OhmU = 75 ohm *Example:* UCBBJR29 is a 75 ohm circuit board bulkhead jack right angle BNC, while the CBBJR29 is a 50 ohm version.

#### Nomenclature





## FAMILY (SERIES) TWINAX/TRIAX

| 30   | =   | TWBNC              |
|------|-----|--------------------|
| 330  | =   | Threaded TWBNC     |
| 70   | =   | TRB                |
| 370  | =   | Threaded TRB       |
| 80   | =   | TRN                |
| 80   | =   | Threaded TRN       |
| 150  | =   | TRS                |
| 3150 | ) = | Threaded TRS (TRT) |
| 450  | =   | Databus            |
| 3450 | ) = | Threaded Databus   |
|      |     |                    |

## TWINAX/TRIAX PREFIX

No Prefix for impedance Twinax and Triax Connectors are non-constant impedance connectors